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Serial No. 09/932,330

Application of: Jon M. Huppenthal, Thomas R. Seeman, Lee A. Burton

Filed: August 17, 2001

Art Unit: 2152

Examiner: Trong Nhan P. Nguyen

Attorney Docket No. SRC012

For: SWITCH/NETWORK ADAPTER PORT FOR CLUSTERED COMPUTERS
EMPLOYING A CHAIN OF MULTI-ADAPTIVE PROCESSORS IN A DUAL IN-LINE
MEMORY MODULE FORMAT

Confirmation No.: 4801

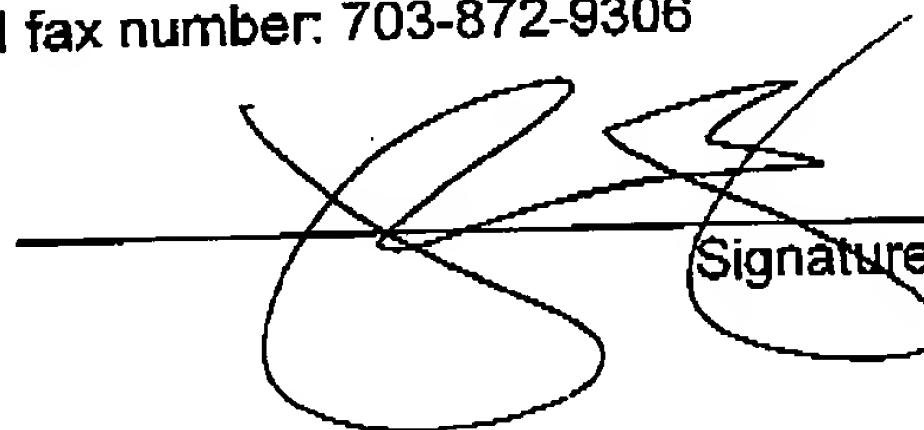
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1. Amendment in response to the Office Action dated December 3, 2004

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<p>Serial No. 09/932,330</p> <p>Application of: Jon M. Huppenthal, Thomas R. Seeman, Lee A. Burton</p> <p>Filed: August 17, 2001</p> <p>Art Unit: 2152</p> <p>Examiner: Trong Nhan P. Nguyen</p> <p>Attorney Docket No. SRC012</p> <p>For: SWITCH/NETWORK ADAPTER PORT FOR CLUSTERED COMPUTERS EMPLOYING A CHAIN OF MULTI-ADAPTIVE PROCESSORS IN A DUAL IN-LINE MEMORY MODULE FORMAT</p>	<p>Confirmation No.: 4801</p> <p>Customer No.: 25235</p> <p>RECEIVED CENTRAL FAX CENTER JAN 25 2005</p>
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AMENDMENT

MAIL STOP AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the office communication mailed December 3, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.